



Attorney Docket No.: YOR920030428US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): Brian A. Floyd
Docket No.: YOR920030428US1
Serial No.: 10/671,984
Filing Date: September 26, 2003
Group: 2817
Examiner: To Be Assigned

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature: Lisa L. Vulpis Date: January 30, 2004

Title: Bypass Switch Topology for Low-Noise Amplifiers

TRANSMITTAL OF FORMAL DRAWINGS

Mail Stop PGPUB Drawings
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant submits herewith five (5) sheets of formal drawings in the above-referenced patent application.

Respectfully submitted,

William E. Lewis

Date: January 30, 2004

William E. Lewis
Attorney for Applicant(s)
Reg. No. 39,274
Ryan, Mason & Lewis, LLP
90 Forest Avenue
Locust Valley, NY 11560
(516) 759-2946